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## **Computer Architecture Exam Solutions**

EXAM 1 SOLUTIONS  
Problem 2 (20 points)  
You designed a microprocessor. It came back from the fab with an error: one of the bits is stuck. We

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call the bit a stuck-at-0 fault if the bit is always 0 (i.e., you cannot store a 1 in it).

## **EXAM 1 SOLUTIONS**

CSE 490/590 Computer  
Architecture Midterm  
Solution. DIRECTIONS

Time limit: 45 minutes  
(12pm - 12:45pm)

There are 40 points  
plus 5 bonus points.

This is a closed-book,  
no calculator, closed-  
notes exam. Each  
problem starts on a

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new page. Please use a pen, not a pencil.

**CSE 490/590**  
**Computer**  
**Architecture**  
**Midterm Solution**

Exam 1: (Fri. 3/20)

Note that Exam 1 covers all material covered until Spring Break, including those covered in lectures, homeworks, labs, along with Homework 4 and Lab 4. Also note that Homework 4 material

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is synergistic with the Exam 1 material, so we strongly suggest that you do Homework 4 as you prepare for the exam.

## **exams [18-447 Introduction to Computer Architecture ...**

CSE 30321 - Computer  
Architecture I - Fall  
2010 Final Exam  
December 13, 2010  
Test Guidelines: 1.  
Place your name on

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EACH page of the test  
in the space provided.

2. Answer every  
question in the space  
provided. If separate  
sheets are needed,  
make sure to include  
your name and clearly  
identify the problem  
being solved. 3. Read  
each question  
carefully.

**CSE 30321 -  
Computer  
Architecture I - Fall  
2010 Final Exam ...**



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EEL 4713 - Computer  
Architecture Final  
Exam Friday, May 5th,  
2006 NAME: Please  
read each question  
carefully, to avoid any  
confusion. This exam  
should have a total of  
14 pages printed  
double-sided (pages 13  
and 14 are scratch  
space). Before you  
begin, make sure your  
copy contains all  
pages. The exam is  
closed book, closed  
notes. Each question

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has its  
Solutions

**EEL 4713 - Computer  
Architecture Final  
Exam**

CS252 Graduate  
Computer Architecture  
Midterm 1 Solutions  
Part A: Branch  
Prediction (22 Points)  
Consider a fetch  
pipeline based on the  
UltraSparc-III processor  
(as seen in Lecture 5).  
In this part, we  
evaluate the impact of  
branch prediction on

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the processor's performance. Assume there are no branch delay slots. A PC Generation/Mux

## **CS252 Graduate Computer Architecture Midterm 1 Solutions**

a step by step procedure, guaranteed to terminate, each step i.... at a distance from the specific computer, "machine independent". tied to

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the computer on which  
the programs will  
execute. the  
mechanisms the  
computer can use to  
figure out where  
operan....

## **exam 1 computer architecture Flashcards - Quizlet**

COMP 212 Computer  
Architecture Mid-term  
Exam Fall 2008 To be  
fair, please do NOT  
open the exam book,  
until told so, Notice:

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Mid-term is close book, close notes, NO calculator and NO discussions. Please write down the details of your solutions, partial results will be given partial credits. Don't rush, you should have plenty of time, do a careful

## **COMP 212 Computer Architecture Mid-term Exam Fall 2008**

Free Computer General Knowledge-

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## **Computer System Architecture | Questions & Answers**

The Architect  
*Page 14/26*

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## **exams [Computer Architecture - Fall 2017]**

Solution: The lowest  
possible cycle time for



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the pipelined version of this circuit is 2. This is true because the component with the longest latency has latency 2. During a cycle, all components must have a chance to finish their computation, so the cycle time needs to be at least 2 to allow the slowest components to finish.

### **CS152 COMPUTER ARCHITECTURE AND**

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**ENGINEERING  
EXAMINATION #2 ...**

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| Electrical ...**

Computer Architecture  
1 Fall 2011 Final Exam  
Solutions, Uppsala  
University Page 7 of 8

7. Calculate the effective CPI (taking into account both instruction execution and memory access) for the following unified instruction and data cache. [7 points]

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Instruction execution  
CPI = 1.0 Percent of  
instructions that are  
loads or stores = 33%

## **Dark 1 HT2011 Exam Solutions - Uppsala University**

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computing • For you •  
As computer scientists,  
software engineers,  
and sophisticated  
users, understanding  
how computers work is  
essential • The  
processor is the most  
important piece of this  
story • Many  
performance (and  
efficiency) problems  
have their roots in  
architecture.

**cse141: Introduction  
to Computer**  
*Page 23/26*

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## **Architecture**

4. The exam is open book and open notes.
  5. All other points of the ND Honor Code apply. By writing your name on the exam, you agree to abide by the ND Honor Code.
  6. Upon completion, please turn in the test and any scratch paper that you used.
- Suggestion: -  
Whenever possible, show your work and your thought process.



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This will make it easier  
for us to

## **2010 Midterm Key - University of Notre Dame**

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Exam Solution Spring  
2014 from CS 16.482  
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Massachusetts, Lowell.  
16.482 / 16.561:  
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and Design Spring  
2014 Final Exam  
Solution 1. (19

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